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Via Electronic Filing

The Honorable Martin Oberman, Chairman
Surface Transportation Board
395 E Street, SW
Washington, D.C. 20423-0001

The Honorable Michelle Schultz, Vice Chairman
Surface Transportation Board
395 E Street, SW
Washington, D.C. 20423-0001

The Honorable Patrick Fuchs
Surface Transportation Board
395 E Street, SW
Washington, D.C. 20423-0001

The Honorable Robert Primus
Surface Transportation Board
395 E Street, SW
Washington, D.C. 20423-0001

The Honorable Karen Hedlund
Surface Transportation Board
395 E Street, SW
Washington, D.C. 20423-0001

Re: **Docket No. EP 770(1) – *Urgent Issues in Freight Rail Service---Railroad Reporting***

Dear Chairman Oberman, Vice Chairman Schultz, Board Members Fuchs, Primus, and Hedlund,

Per the Surface Transportation Board's May 5, 2022 decision, Union Pacific is respectfully enclosing its twenty-fifth weekly report on the following performance metrics:

1. Weekly Average Terminal Dwell Times for the eleventh to twentieth largest terminals;
2. Weekly Average Number of Train Starts Per Day (sorted by train type);
3. Inventory breakdown by privately-owned, TTX-owned, and railroad-owned cars;¹
4. Weekly average recrew rate and rate for each "operating division;"
5. Industry Spot & Pull percentage for the system and each "operating division;"

¹ Union Pacific is actively auditing this metric. Union Pacific may amend the data for this metric later after completing its quality control review.



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6. Weekly average number of local (L) trains cancelled per day and the aggregate number of local trains cancelled per week, broken down by cause (crew and others);² and,
7. Trip Plan Compliance (“TPC”): Original Estimated Time of Arrival (“ETA”) +24 hours.³

Please contact me if you have any questions.

Regards,

Whitney C. Larkin

Enclosure

cc: Ms. Cynthia T. Brown, Chief, Section of Administration, Office of Proceedings

² Union Pacific does not track data surrounding locomotive power in this metric. Therefore, this value is “0” on the spreadsheet.

³ TPC Intermodal is measured at the box level.